

We claim:

1. ~~A method of driving a display apparatus, in~~
which a frame is composed of n subfields, each subfield
has at least an address period to select cells to be
5 displayed and a light period to light the selected cells,
and the gradation scale is represented by combining the
subfields to be lit among said plural subfields, wherein
the subfields of B_n brightness and B_{n-1} brightness, when
the brightness of said n subfields is assumed to be B_i (i
10 $= 1 - n$; $B_1 \leq B_2 \dots B_{n-1} \leq B_n$), are arranged at the
interval of about half the length of said frame.

2. A method of driving a display apparatus as set
forth in claim 1, wherein, the subfields of B_{n-2}
brightness and B_{n-3} brightness among said n subfields are
15 arranged at the interval of about half the length of said
frame so that each subfield is positioned almost at the
midpoint between said two most brightness-weighted
subfields.

3. A method of driving a display apparatus as set
forth in claim 1, wherein, when a rest period occurs in
said frame because the total length of said plural
subfields is shorter than that of said frame, said rest
period is divided into plural rest periods and the
divided rest periods are arranged between said different
25 plural subfields.

4. A method of driving a display apparatus as set
forth in claim 3, wherein, said rest period is divided so
that the number of the divided rest periods is equal to
that of said plural subfields and provided in each
30 subfield.

5. A method of driving a display apparatus as set
forth in claim 1, wherein, the brightness of each
subfield is determined by the number of lit pulses in
said light period, and when the total number of lit
35 pulses in a frame is varied, the original clock
frequency, which generates an execute signal at least
either in said address period or in said light period, is

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varied.

5 6. A method of driving a display apparatus as set forth in claim 5, wherein, only the original clock frequency to generate an execute signal in said address period is varied to vary the period of lit pulses to be applied in said light period.

10 7. A method of driving a display apparatus, in which a frame is composed of plural subfields, each subfield has at least an address period to select cells to be displayed and a light period to light the selected cells, and the gradation scale is represented by combining the subfields to be lit among said plural subfields, wherein when the total length of said plural subfields is shorter than that of said frame and a rest period occurs in said frame, said rest period is divided into plural rest periods and arranged between said different subfields.

15 8. A method of driving a display apparatus as set forth in claim 7, wherein, said rest period is divided so that the number of divided rest periods is equal to that of said plural subfields and provided in each subfield.

20 9. A method of driving a display apparatus, in which a frame is composed of plural subfields, each subfield has at least an address period to select cells to be displayed and a light period to light the selected cells, and the gradation scale is represented by combining the subfields to be lit among said plural subfields, wherein said plural subfields are classified into a front subframe and a rear subframe so that one of the two most brightness-weighted subfields belongs to said front frame and the other belongs to said rear frame, and the interval between the start timings of said front frame and said rear frame is fixed.

25 30 35 10. A method of driving a display apparatus, in which a frame is composed of plural subfields, each subfield has at least an address period to select cells to be displayed and a light period to light the selected

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cells, and the gradation scale is represented by combining the subfields to be lit among said plural subfields, wherein when the total number of lit pulses in a frame is varied, the original clock frequency, which generates an execute signal at least either in said address period or said light period, is varied.

11. A method of driving a display apparatus as set forth in claim 10, wherein, only the original clock frequency to generate an execute signal in said address period is varied to vary the period of lit pulses to be applied in said light period.

12. A method of driving a display apparatus, in which a frame is composed of plural subfields, each subfield has at least an address period to select cells to be displayed and a light period to light the selected cells, and the gradation scale is represented by combining the subfields to be lit among said plural subfields, wherein plural arrangement orders of said plural subfields in said frame are memorized in accordance with the types of image to be displayed, and display is performed with an arrangement order in said subfield selected from said plural arrangement orders according to the judged type of the image.

13. A display apparatus displaying gradation scale by a subfield method in which a frame is composed of n subfields, each subfield has at least an address period to select cells to be displayed and a light period to light the selected cells, and the gradation scale is represented by combining the subfields to be lit among said plural subfields, being characterized in that the subfields of B_n brightness and B_{n-1} brightness, when the brightness of said n subfields is assumed to be B_i ($i = 1 - n$; $B_1 \leq B_2 \dots B_{n-1} \leq B_n$), are arranged at the interval of about half the length of said frame.

14. A display apparatus as set forth in claim 13, wherein, the subfields of B_{n-2} brightness and B_{n-3} brightness among said n subfields are arranged at the

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5 represented by combining the subfields to be lit among said plural subfields, being characterized in that plural arrangement orders of said plural subfields in said frame are memorized in accordance with the types of image to be displayed, and display is performed with an arrangement order in said subfield selected from said plural arrangement orders according to the judged type of the image.

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